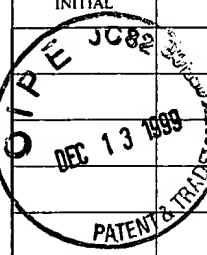
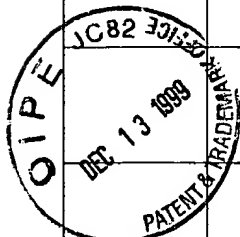


FORM PTQ-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. 30585/3		SERIAL NO. 09/385,394	
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)					APPLICANT John S. Yates, Jr. et al.		RECEIVED DEC 15 1999	
					FILING DATE August 30, 1999			
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		3,781,823	12/25/73	Senese	340	172.5	July 28, 1972	
		4,084,235	04/11/78	Hirtle	364	200	Apr. 14, 1975	
		4,455,602	06/19/84	Baxter	364	200	May 22, 1981	
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		5,638,525	06/10/97	Hammond	395	385	Feb. 10, 1995	
		5,822,578	10/13/98	Frank	395	591	June 5, 1995	
		5,832,205	11/03/98	Kelly	395	185.06	Aug. 20, 1996	
		5,920,721	7/6/99	Hunter	395	705	Jun. 11, 1997	
		5,930,509	07/27/99	Yates	395	707	Jan. 29, 1996	
		5,958,047	09/28/99	Panwar	712	237	Jun. 25, 1997	
Duplicate See paper #6 Filed 11/6/2000								
FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO	
		WO 96/24895	08/15/96	PCT/US	G06F	9/22		
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)								
		Burroughs, Preliminary Edition, B 1700 Systems Reference Manual, Burroughs Corporation (1972)						
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				FILING DATE August 30, 1999	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)					
			Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64 Technology and Market Forecast, IA-64 Software Model - Chapter 3 excerpt Microprocessor Report, MicroDesign Resources (July 20, 1998)		
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			Linley Gwennap, First Merced Patent Surfaces, Intel Document Reveals Processors With Dual Instruction Sets, Microprocessor Report, MicroDesign Resources (July 20, 1998)		
			Raymond J. Hookway, et al., Combining Emulation and Binary Translation (August 28, 1997) http://www.digital.com/info/DTJP01HM.HTM		
			Harold W. Lawson, Jr., et al., Functional Characteristics of a Multilingual Processor, IEEE Transactions on Computers, Vol. C-20, No. 7, pp. 732-742 (July, 1971)		
			Nanodata Corporation, QM-1 Hardware level User's Manual (1975)		
			Nanodata Corporation, QM-2 Processor (1976)		
			Robert F. Rosin, Contemporary Concepts of Microprogramming and Emulation, Computing Surveys, Vol. 1 No. 4, pp. 197-212 (December 1969)		
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			Standard Computer Corp., IC-9000 Central Processing Unit - Principles of Operation (undated - possibly 1969)		
			W.T. Wilner, Design of the Burroughs B1700, Fall Joint Computer Conference, pp. 489-497 (1972)		
			W.T. Wilner, Burroughs B1700 Memory Utilization, Fall Joint Computer Conference, pp. 579-586 (1972)		
EXAMINER			DATE CONSIDERED		
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					



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U.S. DEPARTMENT OF COMMERCE
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ATTY. DOCKET NO.

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APPLICANT

John S. Yates, Jr., et al.

FILING DATE

August 30, 1999

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<i>RUE</i>	3,781,823	12/25/1973	Senese	340	172.5	July 28, 1972
<i>RUE</i>	4,084,235	04/11/1978	Hirtle	364	200	Apr. 14, 1975
<i>RUE</i>	4,455,602	06/19/1984	Baxter	364	200	May 22, 1981
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<i>RUE</i>	WO 97/50031 Farber	12/31/1997	PCT/US WFO	G06F	9/455		

EXAMINER

Richard Ellis

DATE CONSIDERED

11/31/2002

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				FILING DATE August 30, 1999			
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		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
RLE		WO 98/13740 Wing	04/02/1998	WIPO	G06F	—	
RLE		WO 98/28689 Wing	07/02/1998	WIPO	G06F	11/00	
RCE		WO 98/38575 Klaiber	09/03/1998	WIPO	G06F	11/00	
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RCE		WO 99/03037-Kelly	01/21/1999	WIPO	G06F	11/00	
1				WIPO			
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RLE		Burroughs, Preliminary Edition, B 1700 Systems Reference Manual, Burroughs Corporation (1972)					
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RCE		Digital Equipment Corp., White Paper: How DIGITAL FX!32 Works (September 1997) http://www.digital.com/semiconductor/amt/fx32/fx-white.htm					
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RCE		Intel Corporation, Intel Architecture Software Developer's Manual, <i>passim</i> (1997)					
EXAMINER Richard Ellis				DATE CONSIDERED 11/31/2002			

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RE				Harold W. Lawson, Jr., et al., Functional Characteristics of a Multilingual Processor, IEEE Transactions on Computers, Vol. C-20, No. 7, pp. 732-742 (July, 1971)	
RE				Nanodata Corporation, QM-1 Hardware level User's Manual (1975)	
RE				Nanodata Corporation, QM-2 Processor (1976)	
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RE				Robert F. Rosin, An Environment for Research in Microprogramming and Emulation, Communications of the ACM, Vol. 15 No. 8, pp. 748-760 (August 1972)	
RE				Standard Computer Corp., IC-9000 Central Processing Unit - Principles of Operation (undated - possibly 1969)	
RE				W.T. Wilner, Design of the Burroughs B1700, Fall Joint Computer Conference, pp. 489-497 (1972)	
RE				W.T. Wilner, Burroughs B1700 Memory Utilization, Fall Joint Computer Conference, pp. 579-586 (1972)	
EXAMINER				DATE CONSIDERED	
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